

Notice of References Cited

Application/Control No.

09/908,727

Applicant(s)/Patent Under

Reexamination

OHTANI ET AL.

Examiner

Richard A. Booth

Art Unit

2812

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,891,764	04-1999	Ishihara et al.	438/151
	B	US-5,529,937	06-1996	Zhang et al.	438/471
	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Integration", pages 198, 532-533, Lattice Press, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))
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